



PATENT

I hereby certify that on the date specified below, this correspondence is being deposited with the United States Postal Service as first-class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Date

October 14, 2003

Tamra Miller

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. : 10/651,594

Confirmation No. : not yet assigned

Applicants : Tim Murphy and Lee Gotcher

Filed : August 29, 2003

Attorney Docket No.: 501039.03

Art Unit : not yet assigned

Customer No. : 27,076

Examiner : not yet assigned

Title : METHOD AND SYSTEM FOR ELECTRICALLY COUPLING A CHIP TO CHIP PACKAGE

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. §§ 1.56 and 1.97 through 1.98, applicants wish to make known to the Patent and Trademark Office the references set forth on the attached form PTO-1449 (copies of the cited references, as required under 37 C.F.R. § 1.98, are enclosed). Applicants note that this application was filed after June 30, 2003, and therefore copies of the references cited in the "U.S. Patent Documents" section of the attached form PTO-1449 have not been enclosed. Although the aforesaid references are made known to the Patent and Trademark Office in compliance with applicants' duty to disclose all information they are aware of which is believed relevant to the examination of the above-identified application, applicants believe that their invention is patentable.

I hereby certify that no item set forth on the attached form PTO-1449 was cited in a communication from a foreign patent office in a counterpart foreign application or, to my knowledge, after making reasonable inquiry, was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing of this Information Disclosure Statement.

Please acknowledge receipt of this Information Disclosure Statement and kindly make the cited references of record in the above-identified application.

Respectfully submitted,

DORSEY & WHITNEY LLP



Steven H. Arterberry
Registration No. 46,314

SHA:tlm

Enclosures:

- Postcard
- Form PTO-1449
- Cited Reference (1)

1420 Fifth Avenue, Suite 3400
Seattle, WA 98101
Telephone (206) 903-8800
Facsimile (206) 903-8820

h:\ip\documents\clients\micron technology\1000\501039.03\501039.03 ids master cover 101303.doc

FORM PTO-1449 (REV.7-80)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. 501039.03	APPLICATION NO. 10/651,594
INFORMATION DISCLOSURE STATEMENT <i>(Use several sheets if necessary)</i>		APPLICANT(S) Tim Murphy and Lee Gotcher	
		FILING DATE August 29, 2003	GROUP ART UNIT not yet assigned

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA	5,198,684	03/30/93	Sudo	257	79	
	AB	5,200,631	04/06/93	Austin et al.	257	81	
	AC	5,237,441	08/17/93	Nhu	359	152	
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	AK							
	AL							
	AM							
	AN							
	AO							

OTHER PRIOR ART *(Including Author, Title, Date, Pertinent Pages, Etc.)*

AP	Sclater, Neil, "Random-Access Memories (RAMs)," <u>Electronics Technology Handbook</u> , New York: McGraw-Hill, pp. 178-181, 1999.
AQ	

EXAMINER	DATE CONSIDERED

* EXAMINER: Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).